		ATTY. DOCKET NO. 1778.0200000		APPLICATION NO. 09/836,541		
FORM PTO-1449		FIRST NAMED INVENTOR Ryan C. Kinter				
INFORMATION DISCLOSURE STATEMENT		FILING DATE April 18, 2001		ART UNIT 2183		

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1						
	AB1						
	AC1						
	AD1						
	AE1						
	AF1						
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						

RECEIVED

SEP 3 0 2004

Technology ~~~~ ~~~~ 2100

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
ϑℓ	AL1	0 109 567 A2	05/1984	EPO			N/A
ϑℓ	AM1	0 170 398 A2	02/1986	EPO			N/A
ϑℓ	AN1	WO 95/30187 A1	11/1995	WO			N/A
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	1	Hirata, H., *et al.*, "An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads," ACM SIGARCH Computer Architecture News, Volume 20, Number 2, pgs. 136-145, Association for Computing Machinery (May 1992).
	AS	1	
	AT	1	

EXAMINER		DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

295367_1.DOC